

Please amend the present application as follows:

**Claims**

The following is a copy of Applicants' claims that identifies language being added with underlining ("\_\_\_\_") and language being deleted with strikethrough ("———"), as is applicable:

1-28. (Canceled)

29. (Previously presented) A polyphase filter, comprising:

a first phase splitting filter comprising a first RC network, the first phase splitting filter configured to provide a first output;

a second phase splitting filter comprising a second RC network, the second phase splitting filter configured to provide a second output;

a first variable resistance separate from the first RC network and the second RC network, the first variable resistance connected across the first output; and

circuitry capable of detecting the phase of the outputs produced by the first and second outputs, and circuitry capable of adjusting the first variable resistance to produce a desired phase difference between the first output and the second output.

30. (Previously presented) The filter of claim 29, wherein the first output and the second output are single-ended outputs.

31. (Previously presented) The filter of claim 29, wherein the first and the second outputs are differential outputs.

32. (Previously presented) The filter of claim 29, comprising a second variable resistance separate from the first RC network and the second RC network and connected across the second output.

33. (Previously presented) The filter of claim 32, wherein the first variable resistance and the second variable resistance include transistors.

34. (Previously presented) The filter of claim 33, wherein the transistors include at least one MOSFET transistor operated in the linear range.

35. (Previously presented) The filter of claim 32, wherein the first variable resistance and the second variable resistance include a bipolar differential pair.

36. (Previously presented) The filter of claim 32, wherein the first variable resistance and the second variable resistance include a digitally switchable resistance.

37. (Previously presented) The filter of claim 29, wherein the circuitry capable of detecting the phase of the outputs includes a phase detector, an integrator and a differential amplifier.

38. (Previously presented) The filter of claim 29, wherein the circuitry capable of detecting the phase of the outputs includes a phase detector, an integrator, a differential amplifier, a capacitor and a controller for selectively storing and holding the output of the differential amplifier in the capacitor.

39. (Previously presented) The filter of claim 29, wherein a local oscillator provides a fixed frequency signal to inputs of the first phase splitting filter and the second phase splitting filter,

an RF test signal source provides a test signal which is mixed with the first output and the second output to produce an I-based band signal and a Q-based band signal,

the circuitry capable of detecting the phase of the outputs including a phase detector which detects phase differences between the I-based band signal and the Q-based band signal, and an integrator, and

the circuitry capable of adjusting the first variable resistance includes a differential amplifier having an input connected to the output of the integrator, the differential amplifier producing an output to the first variable resistance.

40. (Previously presented) The filter of claim 29, wherein a local oscillator provides a fixed frequency signal to inputs of the first phase splitting filter and the second phase splitting filter,

an RF test signal source provides a test signal which is mixed with the first output and the second output to produce a first baseband signal and a second baseband signal,

the circuitry capable of detecting the phase of the outputs includes a phase detector which detects phase differences between the first baseband signal and a second baseband signal, and an integrator, and

the circuitry capable of adjusting the first variable resistance includes a differential amplifier having an input connected to the output of the integrator, the differential amplifier producing an output to the first variable resistance when a switch is closed, the differential amplifier output being stored in a capacitor that provides the differential amplifier output to the first variable resistance when the switch is open.

41. (Previously Presented) A polyphase filter, comprising:
- a first phase splitting filter that produces a first output, the first phase splitting filter comprising a first RC network;
  - a second phase splitting filter that produces a second output, the second phase splitting filter comprising a second RC network;
  - a first variable resistance connected across the first output and a second variable resistance connected across the second output, the first variable resistance separate from the first RC network and the second variable resistance separate from the second RC network;
  - and
  - a detector that determines the phase of the first and second outputs, and adjusts the first variable resistance and the second variable resistance to produce a desired phase difference between the first output and the second output.
42. (Previously presented) The filter of claim 41, wherein the first output and the second output are single-ended outputs.
43. (Previously presented) The filter of claim 41, wherein the first and the second outputs are differential outputs.
44. (Previously presented) The filter of claim 41, wherein the first variable resistance and the second variable resistance include transistors.
45. (Previously presented) The filter of claim 44, wherein the transistors include at least one MOSFET transistor operated in the linear range.

46. (Previously presented) The filter of claim 41, wherein the first variable resistance and the second variable resistance include a bipolar differential pair.

47. (Previously presented) The filter of claim 41, wherein the first variable resistance and the second variable resistance include a digitally switchable resistance.

48. (Previously presented) The filter of claim 41, wherein the detector includes a phase detector, an integrator and a differential amplifier.

49. (Previously presented) The filter of claim 41, wherein the detector includes a phase detector, an integrator, a differential amplifier, a capacitor and a controller for selectively storing and holding the output of the differential amplifier in the capacitor.

50. (Previously presented) The filter of claim 41, wherein a local oscillator provides a fixed frequency signal to inputs of the first phase splitting filter and the second phase splitting filter, and

an RF test signal source provides a test signal that is mixed with the first output and the second output to produce an I-baseband signal and a Q-baseband signal,

the detector including a phase detector which detects phase differences between the I-based band signal and the Q-baseband signal, and an integrator, and

the detector further including a differential amplifier having an input connected to the output of the integrator, the differential amplifier producing an output to the first variable resistance and the second variable resistance.

51. (Previously presented) The filter of claim 41, wherein a local oscillator provides a fixed frequency signal to inputs of the first phase splitting filter and the second phase splitting filter, and

an RF test signal source provides a test signal that is mixed with the first output and the second output to produce a first baseband signal and a second baseband signal,

the detector including a phase detector that detects phase differences between the first baseband signal and a second baseband signal, and an integrator,

the detector further including a differential amplifier having an input connected to an output of the integrator, the differential amplifier producing an output to the first variable resistance when a switch is closed, the differential amplifier output being stored in a capacitor that stores the differential amplifier output and provides the differential amplifier output to the first variable resistance when the switch is open.

52. (Previously presented) A communication system, comprising:

a transmitter and a receiver, the transmitter comprising an input, an analog-to-digital converter, a digital signal processor, a digital-to-analog converter and an RF signal generator, the transmitter modulating an RF carrier with a signal provided to the transmitter input and transmitting the modulated RF carrier, the receiver comprising an RF input, a local oscillator, a polyphase filter connected to an output of the local oscillator, the polyphase filter producing first and second outputs from the local oscillator output, a mixer that combines the RF input with the first and second outputs of the polyphase filter, baseband circuitry, an analog-to-digital converter, and a digital signal processor that demodulates an output of the analog-to-digital converter, and produces a demodulated output,

the polyphase filter including:

a first phase splitting filter comprising a first RC network, the first phase splitting filter configured to provide a first output;

a second phase splitting filter comprising a second RC network, the second phase splitting filter configured to provide a second output;

a first variable resistance separate from the first RC network and the second RC network, the first variable resistance connected across the first output; and

circuitry capable of detecting the phase of the outputs produced by the first and second outputs, and circuitry capable of adjusting the first variable resistance to produce a desired phase difference between the first output and the second output.

53. (Previously presented) The system of claim 52, wherein the phase is substantially continuously detected and adjusted in a closed loop manner.



54. (Previously presented) The system of claim 52, wherein the phase is adjusted at predetermined times in an open loop manner.

55. Canceled

56. (Currently amended) A polyphase filter, comprising:

a first phase splitting filter comprising a first RC network, the first phase splitting filter configured to provide a first output, the first RC network including a first resistor in series connection with a first capacitor, the first resistor and the ~~second resistor~~ first capacitor directly connected together at a first node;

a second phase splitting filter comprising a second RC network, the second phase splitting filter configured to provide a second output, the second RC network including a second resistor in series connection with a second capacitor, the second resistor and the second capacitor directly connected together at a second node;

a first variable resistance separate from the first RC network and the second RC network, the first variable resistance connected across the first output, the first variable resistance connected to the first node; and

circuitry capable of detecting the phase of the outputs produced by the first and second outputs, and circuitry capable of adjusting the first variable resistance to produce a desired phase difference between the first output and the second output.